

Electronic Acknowledgement Receipt

EFS ID:	1827337
Application Number:	10003390
International Application Number:	
Confirmation Number:	5348
Title of Invention:	NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A FAIL BIT DETECTING SCHEME AND METHOD FOR COUNTING THE NUMBER OF FAIL BITS
First Named Inventor/Applicant Name:	Jae-Yong Jeong
Customer Number:	20575
Filer:	Jerome S. Marger/Tina Miller
Filer Authorized By:	Jerome S. Marger
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	RequestCertificateCorrection.pdf	63155	no	1

Warnings:

Information:									
2	Request for Certificate of Correction	CertificateCorrection.pdf	51967	no	1				
Warnings:									
Information:									
Total Files Size (in bytes):		115122							
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<p>National Stage of an International Application under 35 U.S.C. 371 If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>									
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